L Number	Hits	Search Text	DB	Time stamp
1	222578	(design\$3 fabricat\$3 manufactur\$3 configur\$3	USPAT	2004/09/03 14:26
		modeling) with (microprocessor		
		microcontroller ic chip (integrated adj1		
_		circuit) semiconductor)		
2	277797	spreadsheet spread-sheet matrix (spread adj1	USPAT	2004/09/03 14:26
3	107764	sheet) (spreadsheet spread-sheet matrix (spread	USPAT	2004/09/03 14:26
3	127764	adjl sheet)) with (creat\$3 generat\$3 form\$7	USPAI	2004/09/03 14:26
		develop\$4)		
4	197671	(information data metadata specification	USPAT	2004/09/03 14:26
_		description defin\$6) with (microprocessor		
		microcontroller ic chip (integrated adj1		
		circuit) semiconductor)		
5	1304	((spreadsheet spread-sheet matrix (spread	USPAT	2004/09/03 14:13
		adj1 sheet)) with (creat\$3 generat\$3 form\$7		
		develop\$4)) with ((information data metadata		
		specification description defin\$6) with		
*		(microprocessor microcontroller ic chip		
_	710	(integrated adj1 circuit) semiconductor))		0001/00/00 14 14
6	710		USPAT	2004/09/03 14:14
		configur\$3 modeling) with (microprocessor microcontroller ic chip (integrated adj1		
		circuit) semiconductor)) and (((spreadsheet		
		spread-sheet matrix (spread adj1 sheet))		
		with (creat\$3 generat\$3 form\$7 develop\$4))		
		with ((information data metadata		
		specification description defin\$6) with		
		(microprocessor microcontroller ic chip		
		(integrated adj1 circuit) semiconductor)))		
7	39	1 , , , , , , , , , , , , , , , , , , ,	USPAT	2004/09/03 14:24
		configur\$3 modeling) with (microprocessor		
		microcontroller ic chip (integrated adj1		
		circuit) semiconductor)) and (((spreadsheet		
		spread-sheet matrix (spread adj1 sheet))		
		<pre>with (creat\$3 generat\$3 form\$7 develop\$4)) with ((information data metadata</pre>		
		specification description defin\$6) with		
		(microprocessor microcontroller ic chip		
		(integrated adj1 circuit) semiconductor))))		
		and default		
8	39	((((design\$3 fabricat\$3 manufactur\$3	USPAT	2004/09/03 14:24
		configur\$3 modeling) with (microprocessor		
		microcontroller ic chip (integrated adj1		
		circuit) semiconductor)) and (((spreadsheet		
		spread-sheet matrix (spread adj1 sheet))		
		<pre>with (creat\$3 generat\$3 form\$7 develop\$4)) with ((information data metadata</pre>		
		specification description defin\$6) with		
		(microprocessor microcontroller ic chip		
		(integrated adj1 circuit) semiconductor))))		
		and default) and (spreadsheet spread-sheet		
		matrix (spread adj1 sheet))		
9	475277	(information data metadata specification	USPAT;	2004/09/03 14:27
		description defin\$6) with (microprocessor	US-PGPUB;	
		microcontroller ic chip (integrated adj1	EPO; JPO;	
		circuit) semiconductor)	DERWENT;	
1.0	60000		IBM_TDB	
10	603334	spreadsheet spread-sheet matrix (spread adj1	USPAT;	2004/09/03 14:27
		sheet)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
11	620799	(design\$3 fabricat\$3 manufactur\$3 configur\$3	IBM_TDB USPAT;	2004/09/03 14:28
	020759	modeling) with (microprocessor	US-PGPUB;	2004/03/03 14:28
		microcontroller ic chip (integrated adj1	EPO; JPO;	
		i i i i i i i i i i i i i i i i i i i	, ,	1
		circuit) semiconductor)	DERWENT;	

	051000			
12	251999	<pre>(spreadsheet spread-sheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/03 14:28
13	2172	((spreadsheet spread-sheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)) with ((information data metadata specification description defin\$6) with (microprocessor microcontroller ic chip	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/03 14:29
14	1098	(integrated adj1 circuit) semiconductor)) ((design\$3 fabricat\$3 manufactur\$3 configur\$3 modeling) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)) and (((spreadsheet spread-sheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)) with ((information data metadata specification description defin\$6) with	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/03 14:29
15	1098	<pre>(microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor))) (((design\$3 fabricat\$3 manufactur\$3 configur\$3 modeling) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)) and (((spreadsheet spread-sheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)) with ((information data metadata</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/03 14:29
		specification description defin\$6) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)))) and (spreadsheet spread-sheet matrix (spread adj1 sheet))		
16	1368	cell with default	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/03 14:29
17	1	((((design\$3 fabricat\$3 manufactur\$3 configur\$3 modeling) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)) and (((spreadsheet spread-sheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)) with ((information data metadata specification description defin\$6) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor))) and (spreadsheet spread-sheet matrix (spread adj1 sheet))) and (cell with default)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/03 14:32
18	56	(spreadsheet spread-sheet matrix (spread adj1 sheet)) and ((design\$3 fabricat\$3 manufactur\$3 configur\$3 modeling) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)) and ((spreadsheet spread-sheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)) and (cell with default)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/03 14:32